

ESD Shielding Bags



Specifications:

Thickness 75µm Outer layer resistivity <math><1011\Omega/sq</math>

Tensile strength 5800psi Metal layer resistivity <math><103\Omega/sq</math>

Static shielding <math><30V</math> Inner layer resistivity <math><1011\Omega/sq</math>

Static discharging time <math><0.03S</math> Attenuation of EMI >10dbv

It is suitable for packing of PCB, IC's and other static sensitive components

This kind of bag can keep static-sensitive components away from the potential static danger to the largest extent.

Its special four-layer structure can create such effect as an inductive cover to separate the goods inside from the static field.

The inner layer is made from ethane which can remove the static and avoid static generation inside the bag.

This kind of hot seal bag is transparent, so the goods inside can be easily recognized from outside. The surface resistance: 108~1011ohm

Physical properties meets international standards ASTM D257, ELA541 and MIL-B-81705.

All sizes are available in open flat, ziplock, and tridimensional styles, any bag for large machines can be customized.

<https://www.samytronic.de/ESD-bag-75-um>